

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/672,960

Title: System And Method For Limited Fanout Daisy Chaining Of Cache  
Invalidation Requests In A Shared-memory Multiprocessor System

Applicant(s): Kourosh Gharachorloo et al.

Filing Date: September 26, 2003

Examiner: CHERY, Mardochee

Art Unit: 2188

Atty Docket. No. 200302257-2

Confirmation No. 9440

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RESPONSE to FINAL OFFICE ACTION mailed September 7, 2007

Mail Stop **AFTER FINAL**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office Action mailed September 7, 2007, Applicants respectfully submit this Response.

**Remarks** begin on page 2.

A **Terminal Disclaimer** is enclosed herein.